

**Amendment to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1-12. (canceled)

13. (Currently Amended) A method for adaptation of a function for controlling an operating sequence of a control unit, the function accessing at least one global variable of at least one program for control, the method comprising:

determining a breakpoint in the at least one program, wherein the determination is based on a comparison at least one of (1) between an address of the control unit and a register content loaded by a load instruction and (2) between the address of the control unit and a destination address of a store instruction;

assigning the global variable address information which is present in at least one memory device;

loading ~~out of~~ the memory device the address information of the global variable by at least one load instruction; and

replacing the address information of the global variable with one of the store instruction and the load instruction.

14. (Previously Presented) The method as recited in claim 13, wherein the address information of the global variable is replaced by address information of a pointer variable.

15. (Previously Presented) The method as recited in claim 14, wherein the address information of the pointer variable is located in a reserved memory area.

16. (Previously Presented) The method as recited in claim 13, further comprising: manipulating a memory instruction onto the global variable by replacing the memory instruction with a jump instruction.

17. (Previously Presented) The method as recited in claim 13, further comprising: determining an initial address of the function from the address information.

18. (Previously Presented) The method as recited in claim 13, further comprising: replacing the function for controlling the operating sequence by replacing the address information with an additional function.

19. (Previously Presented) The method as recited in claim 16, further comprising: the function for controlling the operating sequence by replacing the memory instruction with the jump instruction is replaced with an additional function.

20. (Currently Amended) A device for adopting a function for controlling an operating sequence of a control unit, the function accessing at least one global variable of at least one program for control, the global variable being assigned address information, the device comprising:

- a memory in which the address information is located;

- a control device configured to:

  - determine a breakpoint in the at least one program, wherein the determination is based on a comparison at least one of (1) between an address of the control unit and a register content loaded by a load instruction and (2) between the address of the control unit and a destination address of a store instruction;

  - assign the global variable address information which is present in at least one memory device; and

  - load the address information of the global variable out of the memory by at least one load instruction, the control device configured to replace the address information of the global variable with one of the store instruction and the load instruction.

21. (Previously Presented) The device as recited in claim 20, wherein the control device replaces the address information of the global variable by address information of a pointer variable.

22. (Currently Amended) A control unit for controlling an operating sequence, the control unit comprising:

a device configured to adopt a function to controlling the operating sequence of a control unit, the function accessing at least one global variable of at least one program for control, the global variable being assigned address information, the device comprising:

a memory in which the address information is located;

a control device configured to:

determine a breakpoint in the at least one program, wherein the determination is based on a comparison at least one of (1) between an address of the control unit and a register content loaded by a load instruction and (2) between the address of the control unit and a destination address of a store instruction;

assign the global variable address information which is present in at least one memory device; and

load the address information of the global variable out of the memory by at least one load instruction, the control device configured to replace the address information of the global variable with one of the store instruction and the load instruction.

23. (Currently Amended) A storage medium storing program code executable by a computer, the program code, when executed by the computer causing the computer to perform the steps of:

determining a breakpoint in the at least one program, wherein the determination is based on a comparison at least one of (1) between an address of the control unit and a register content loaded by a load instruction and (2) between the address of the control unit and a destination address of a store instruction;

assigning the global variable address information which is present in at least one memory device;

loading the address information of the global variable by at least one load instruction out of the memory device; and

replacing the address information of the global variable with one of the store instruction and the load instruction.

24. (Currently Amended) A computer program having program code executable by a computer, the program code, when executed by the computer causing the computer to perform the steps of:

determining a breakpoint in the at least one program, wherein the determination is based on a comparison at least one of (1) between an address of the control unit and a register content loaded by a load instruction and (2) between the address of the control unit and a destination address of a store instruction;

assigning the global variable address information which is present in at least one memory device;

loading ~~out of~~ the memory device the address information of the global variable by at least one load; and

replacing the address information of the global variable with one of the store instruction and the load instruction.

25. (New) A method for adapting a function to an operating sequence for controlling a control unit of a device, the function accessing at least one program that includes a breakpoint, the method comprising:

determining the breakpoint in the at least one program, wherein the determination is based on a comparison at least one of (1) between an address of the control unit and a register content loaded by a load instruction and (2) between the address of the control unit and a destination address of a store instruction;

modifying at least one of the load instruction and the store instruction based on at least one of a length of the load instruction, a length of the store instruction, a type of addressing, and a type of store instruction;

creating a hook for the function based on a type of the breakpoint; and

tying the hook for the function to the operating sequence.

26. (New) The method of claim 25, wherein the load instruction loads an address of a global variable.

27. (New) The method of claim 25, wherein the load instruction is replaced by an address of a pointer variable.

28. (New) The method of claim 25, wherein the store instruction is a 32-bit store instruction.

29. (New) The method of claim 28, wherein the 32-bit store instruction is replaced by an absolute jump instruction.

30. (New) The method of claim 28, wherein the 32-bit store instruction is replaced by a function call.

31. (New) The method of claim 28, wherein the 32-bit store instruction is replaced by a 32-bit jump instruction.